using a hardware device to produce a binary true random sequence of signals;

interfacing said binary true random sequence of signals to a general purpose personal computer; and

utilizing said interfaced binary true random sequence of signals in said computer.

58. A method as in claim 57 wherein said step of interfacing comprises providing a device driver.

REMARKS

Applicant respectfully requests that the examiner base the examination upon claims 8 - 20 and 29 - 40 in the attached application, plus the new claims 41 - 58. Claims 1 - 7 and 21 - 28 were elected in U.S. Patent Application No. 08/388,631, the parent application of this divisional. Therefore, claims 1 - 7 and 21 - 28 have been canceled in this application.

An Information Disclosure Statement is enclosed. Each reference listed was cited in the parent application, so a copy is not required in this application.

In view of the foregoing amendment, it is believed that the application, including claims 8 - 20 and 29 - 58, is in condition for allowance, and favorable action is respectfully requested.

No additional fee for this Preliminary Amendment is seen to be required. If any additional fee is required, please charge it to Deposit Account No. 04-1697.

Respectfully submitted,
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